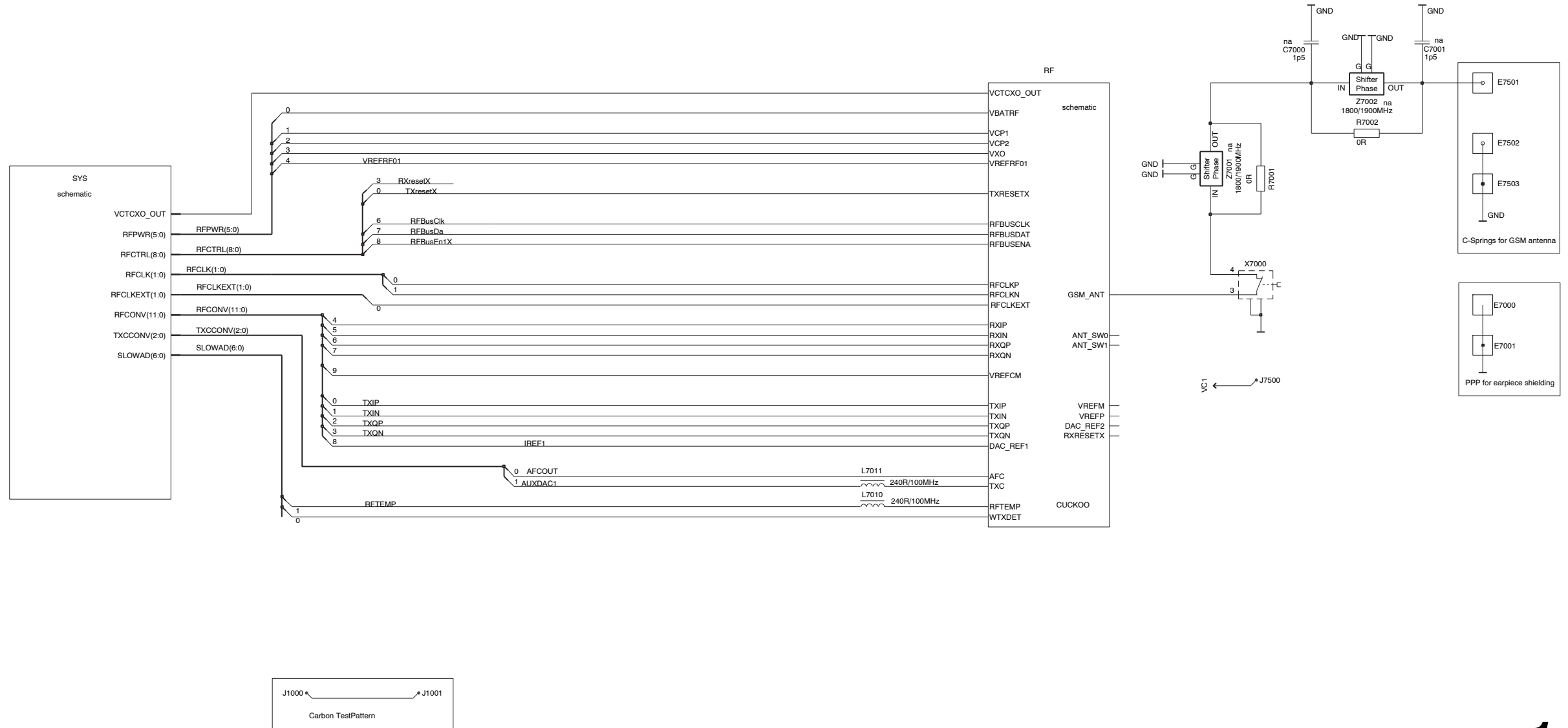
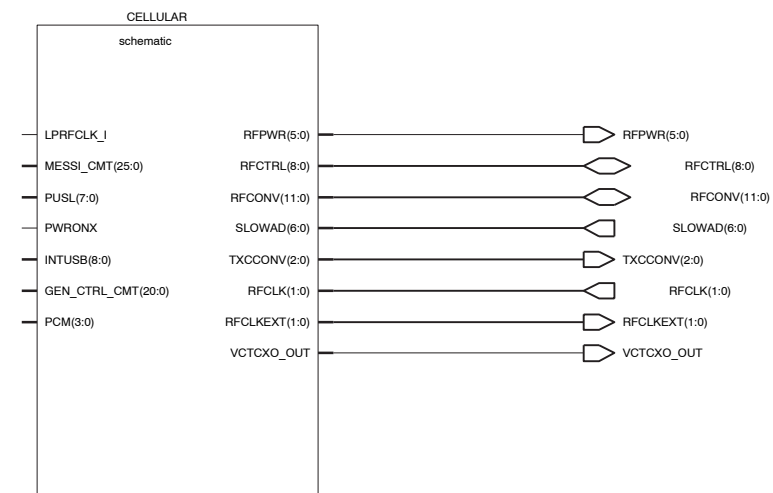
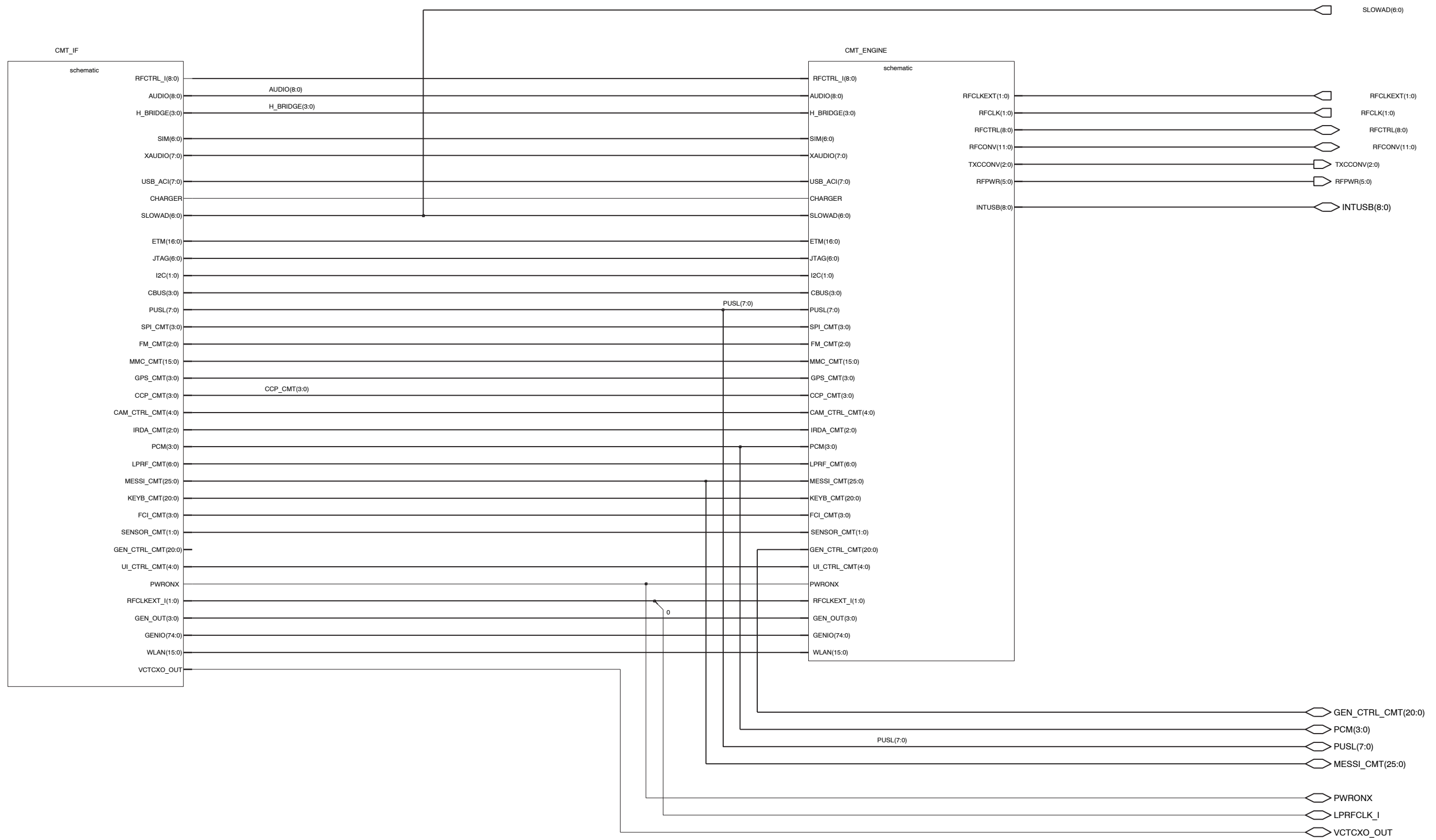
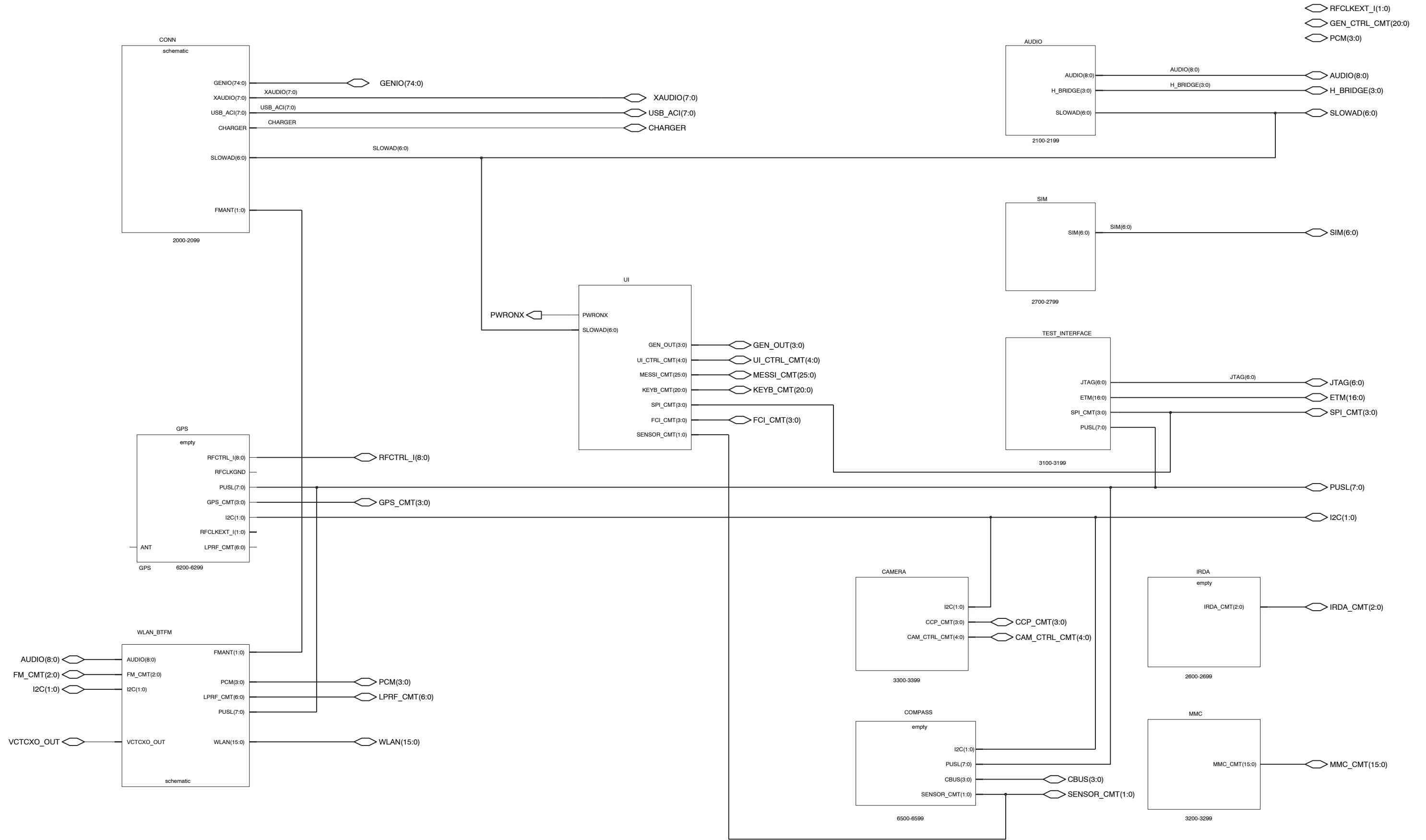


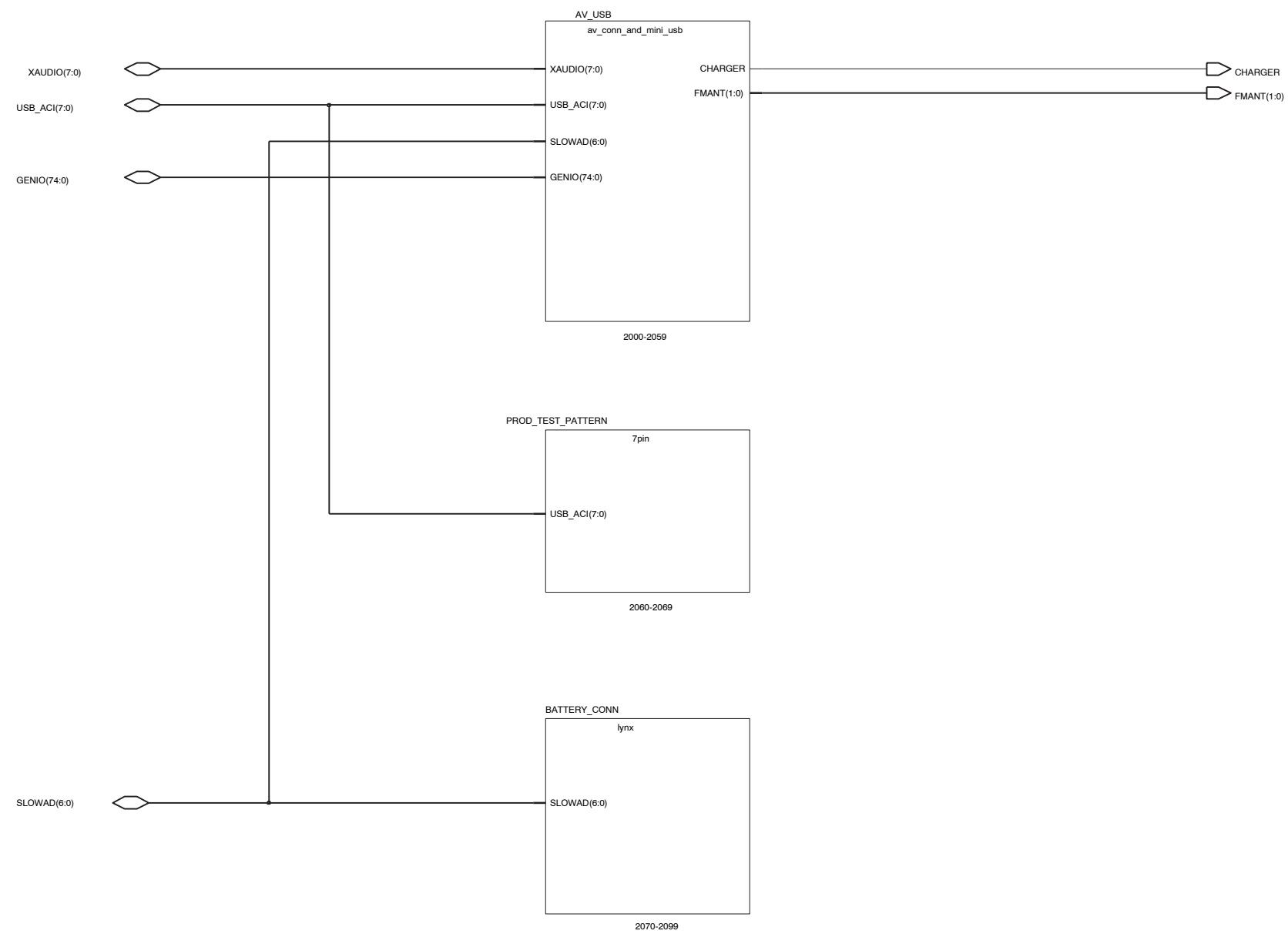
Page:	Description
01:	TOP SHEET
02:	SYS
03:	CELLULAR
04:	CMT IF
05:	SYS AND BATT CONN
06:	AV_USB
07:	PROD TEST PATTERN
08:	BATTERY_CONN
09:	WLAN_BTFM
10:	BTFM_RDS
11:	WLAN_SIZE_4_0
12:	UI
13:	AUDIO
14:	SIM
15:	TEST_INTERFACE
16:	MMC
17:	CAMERA
18:	CMT_ENGINE
19:	POWER
20:	RETU
21:	TAHVO
22:	DIGI
23:	RAP
24:	MEM_CMT
25:	USB_CONN
26:	RF_BB
27:	GENIO_MUX
28:	RF
29:	WING_TOP (only in Wingboard schematic)

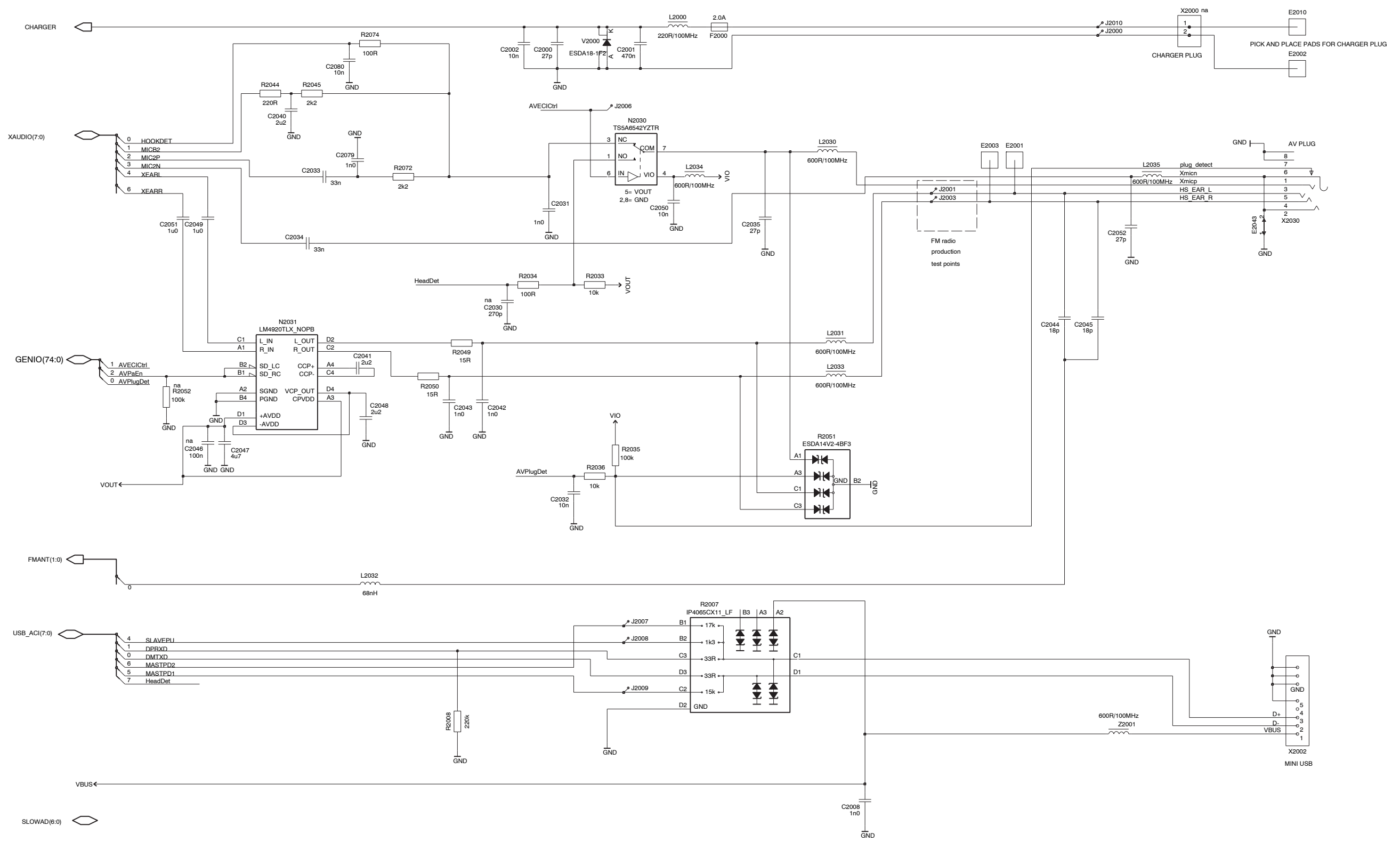


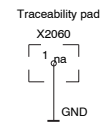




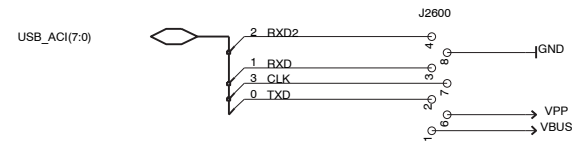


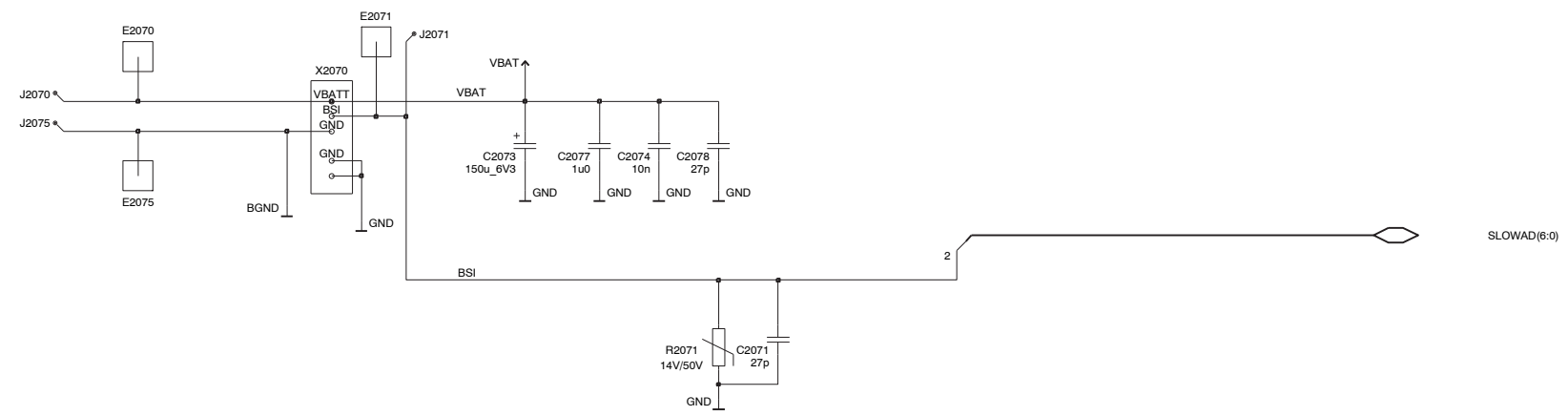




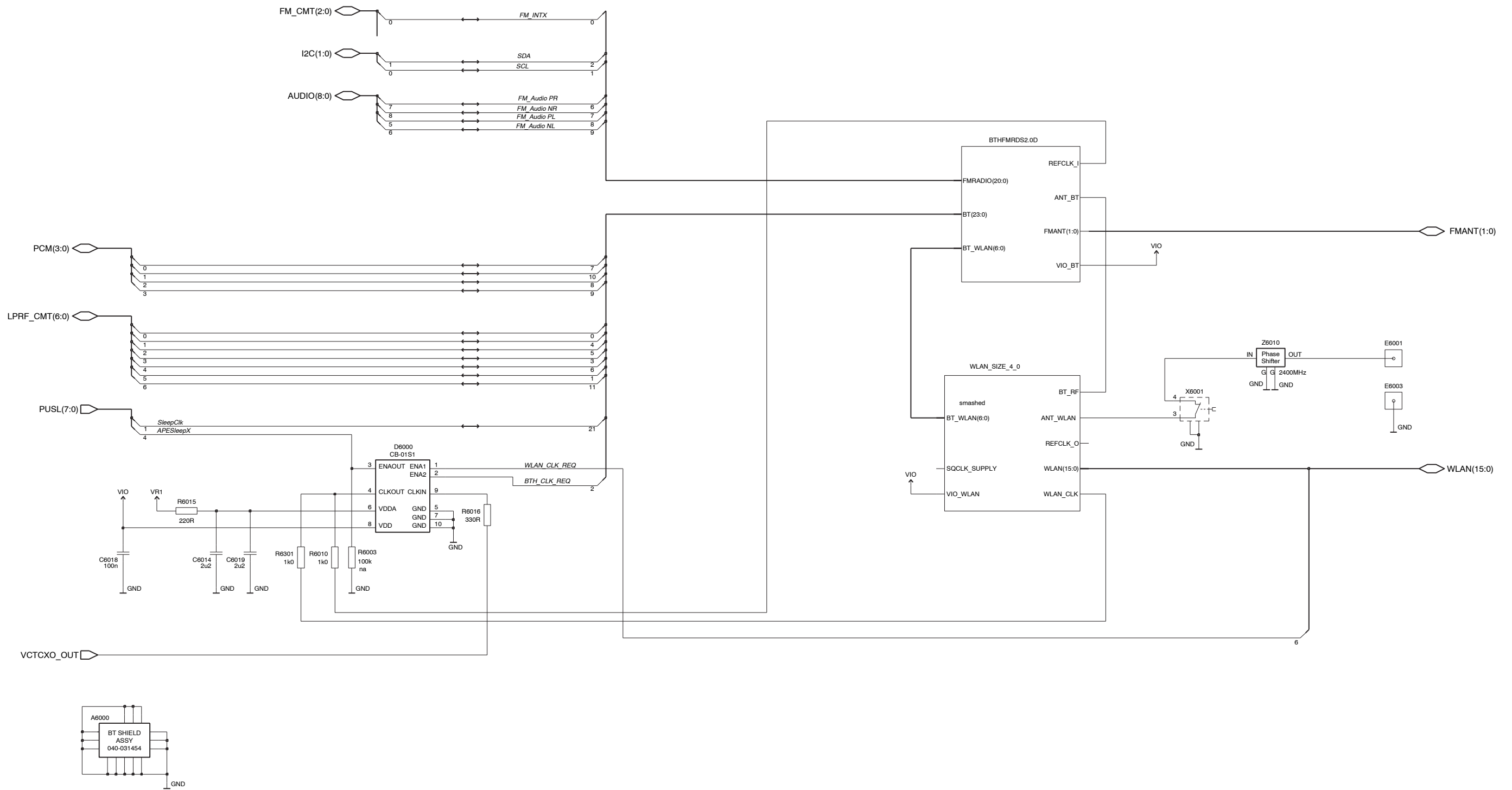


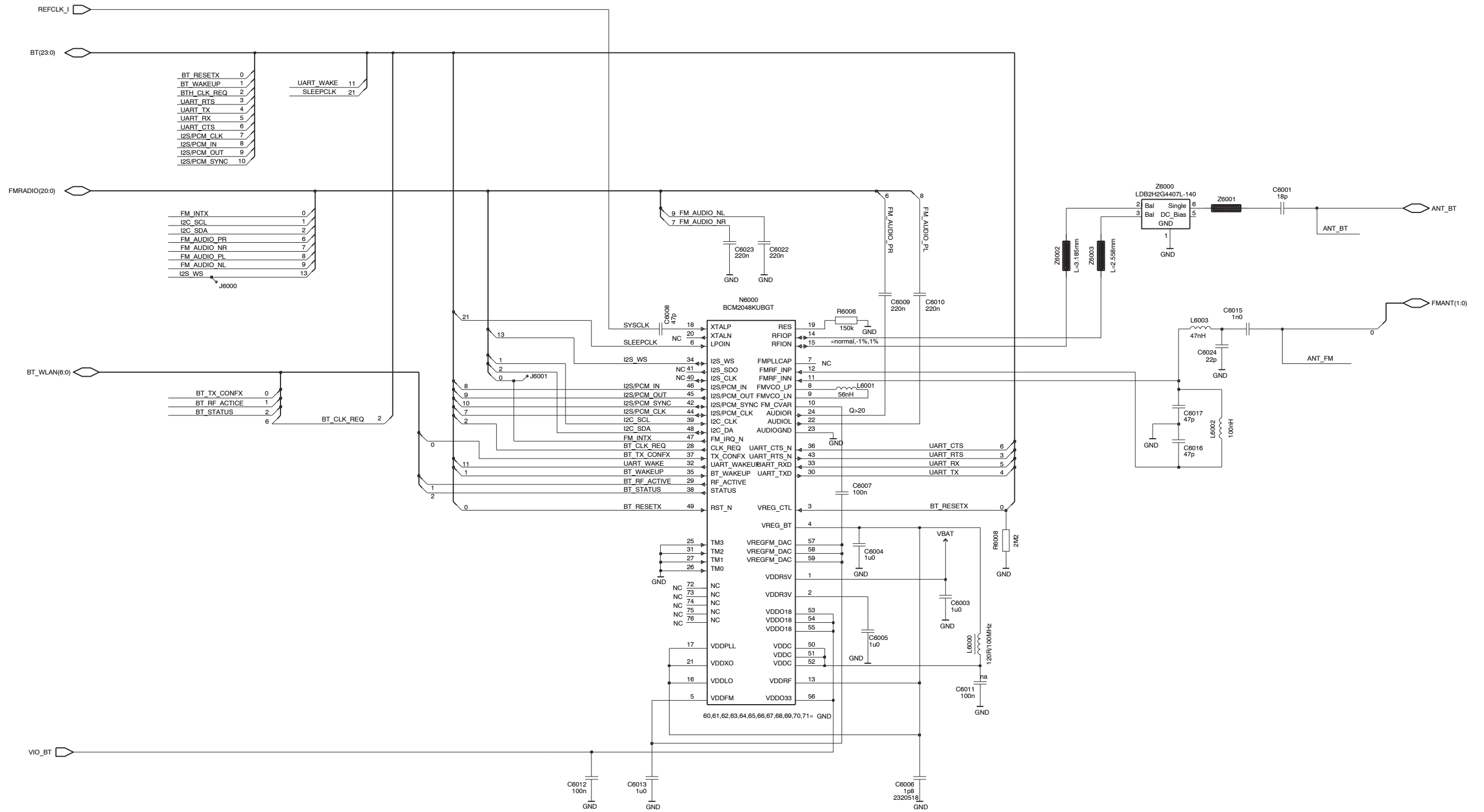
BB5.0 have defined 7-pin test pad pattern

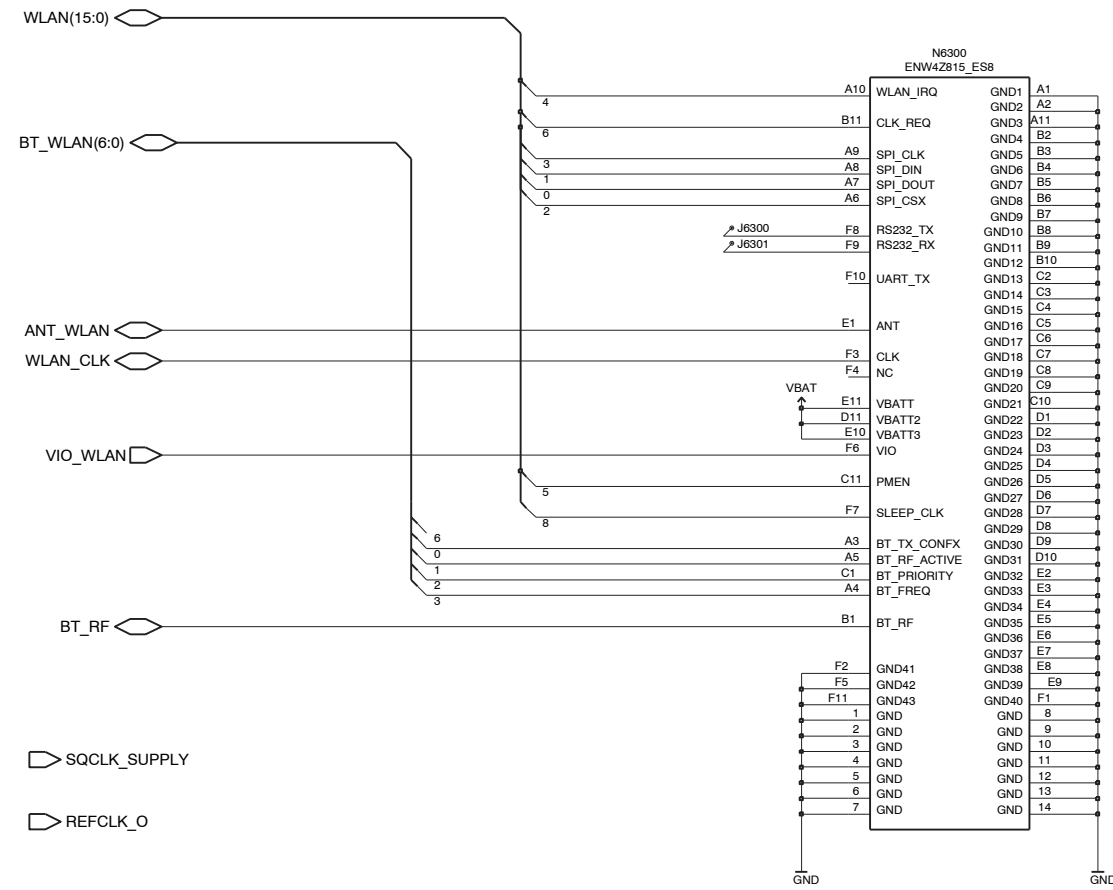


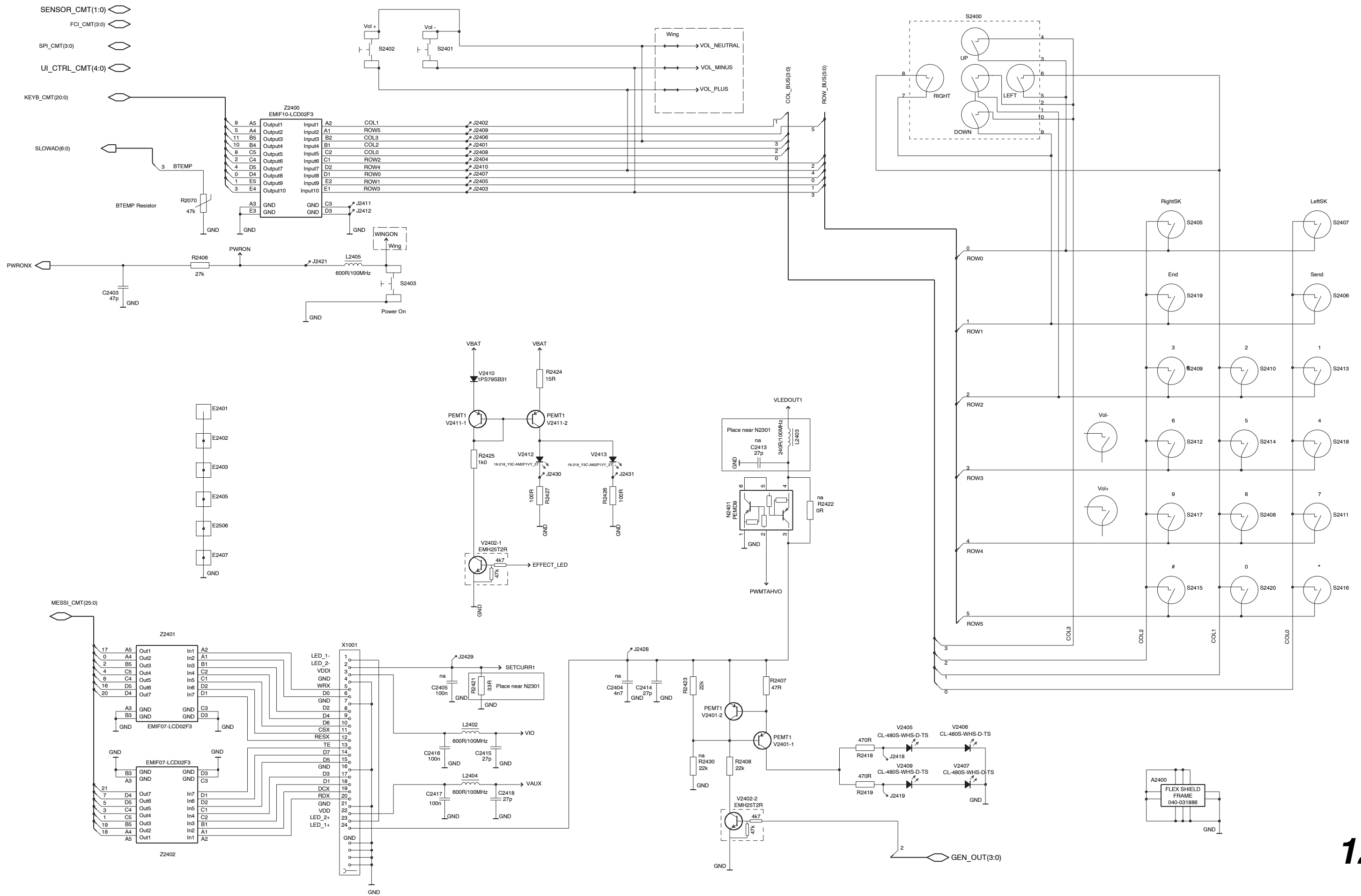


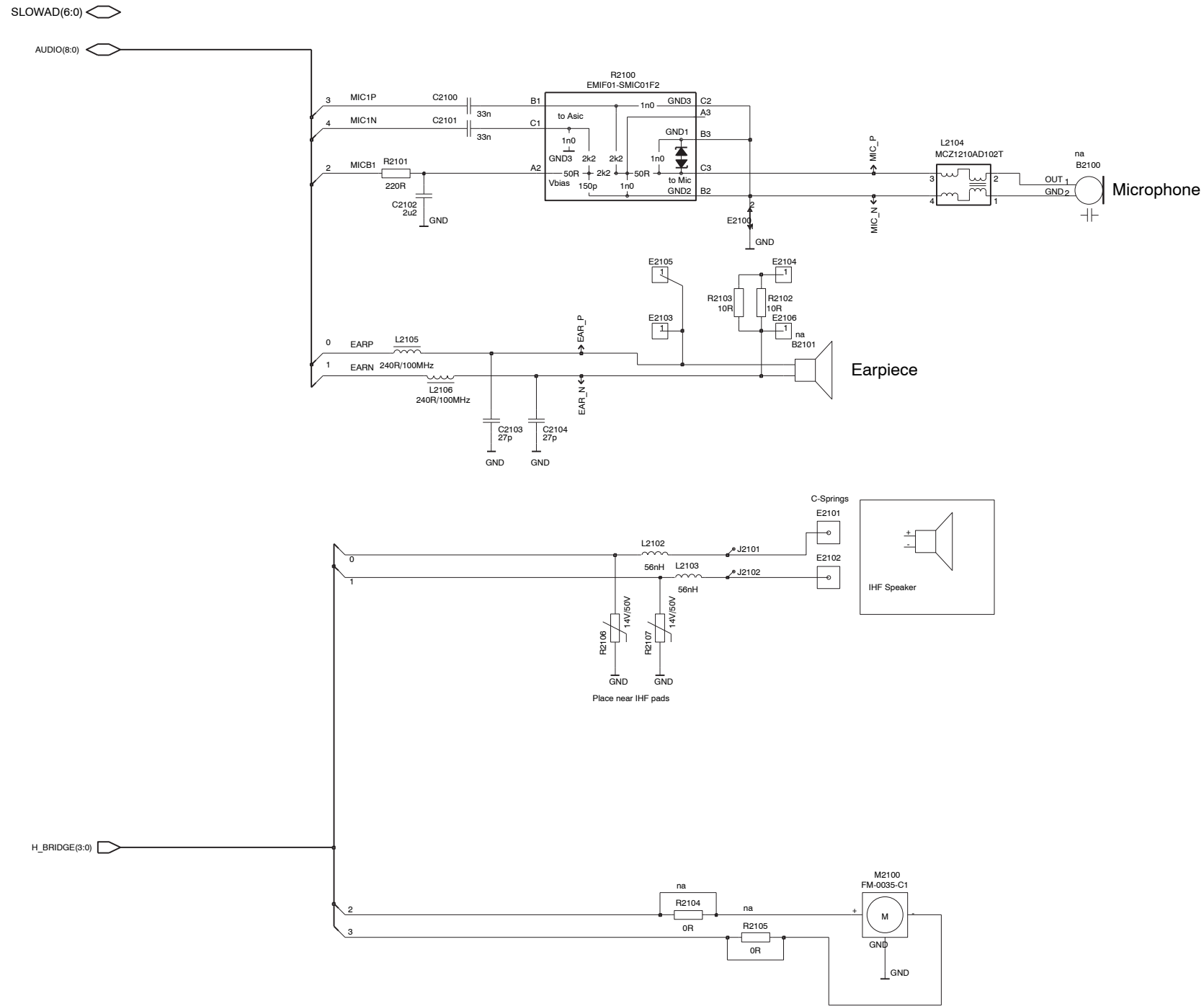


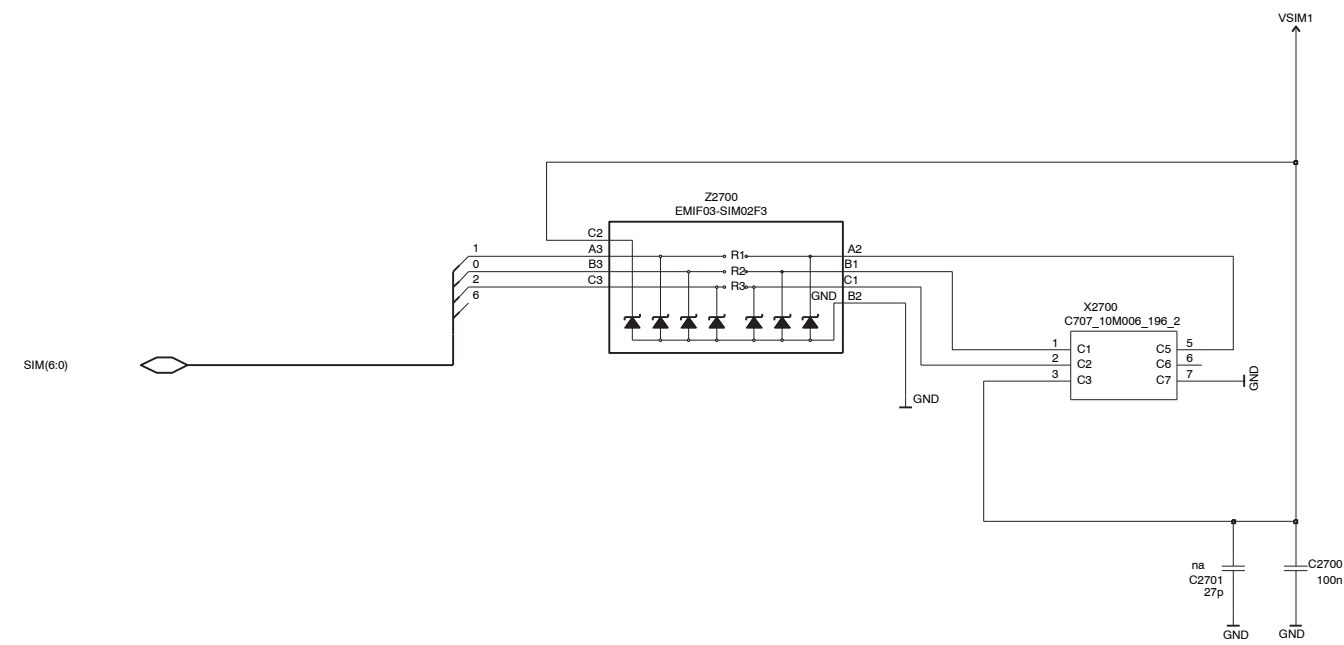


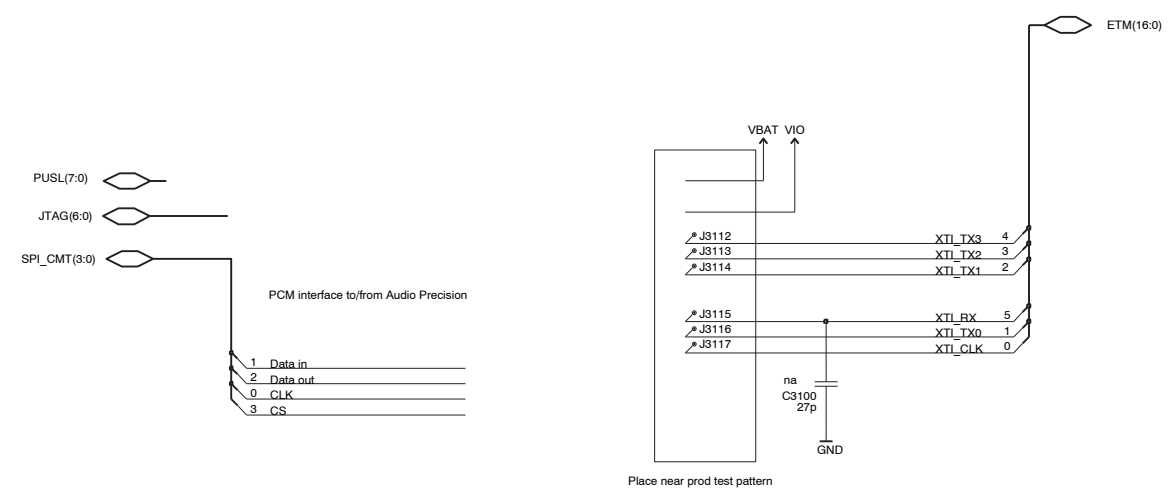


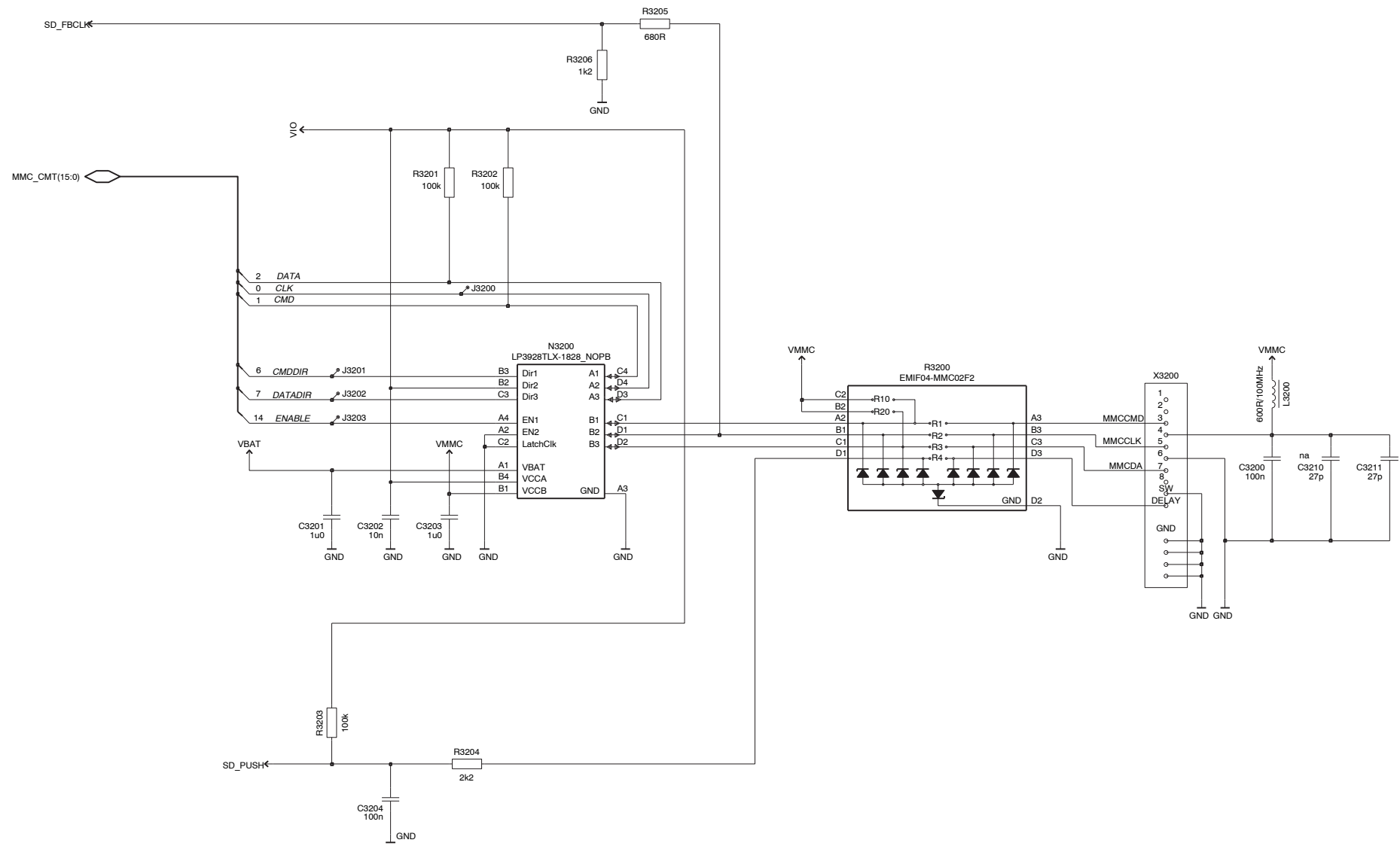




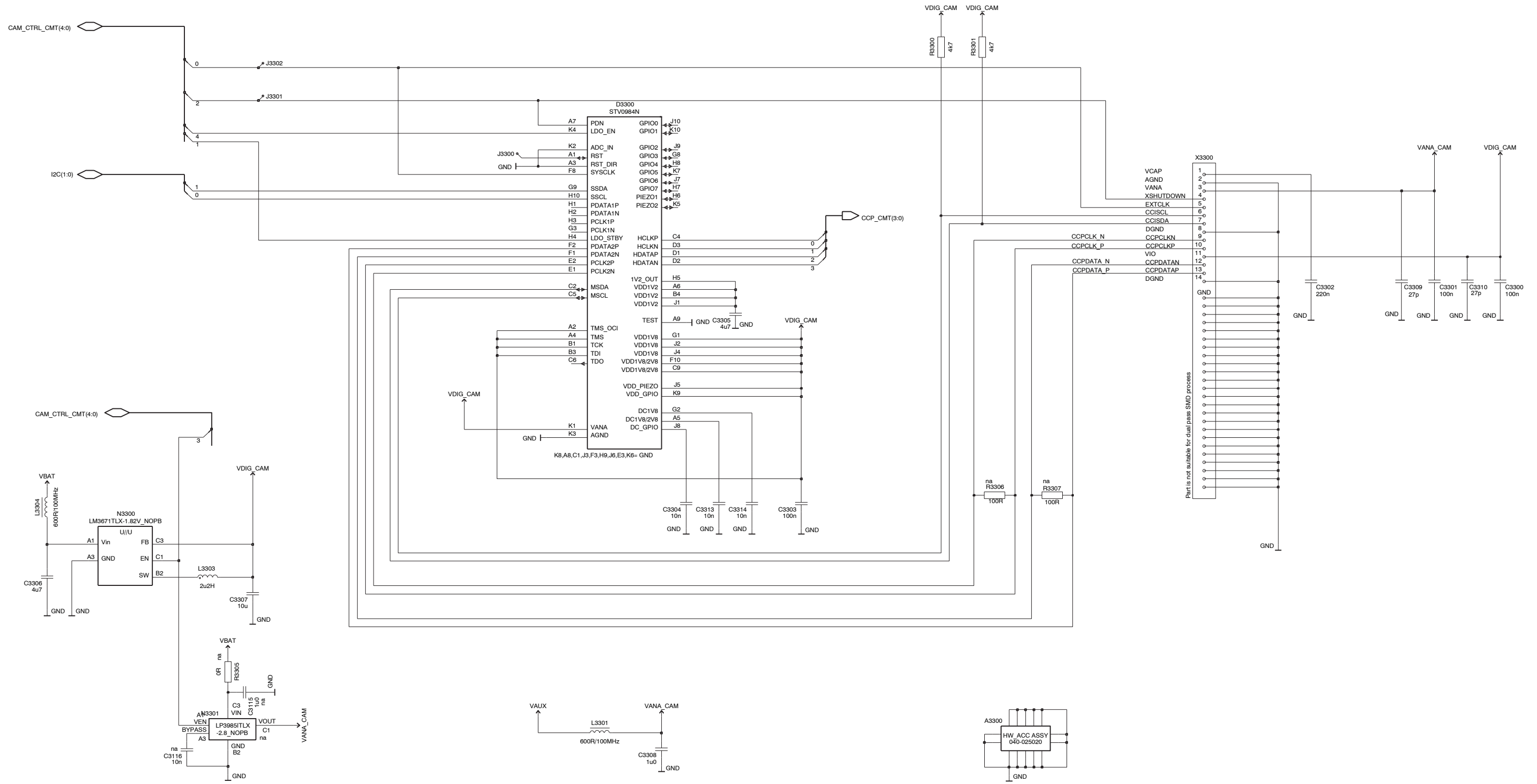


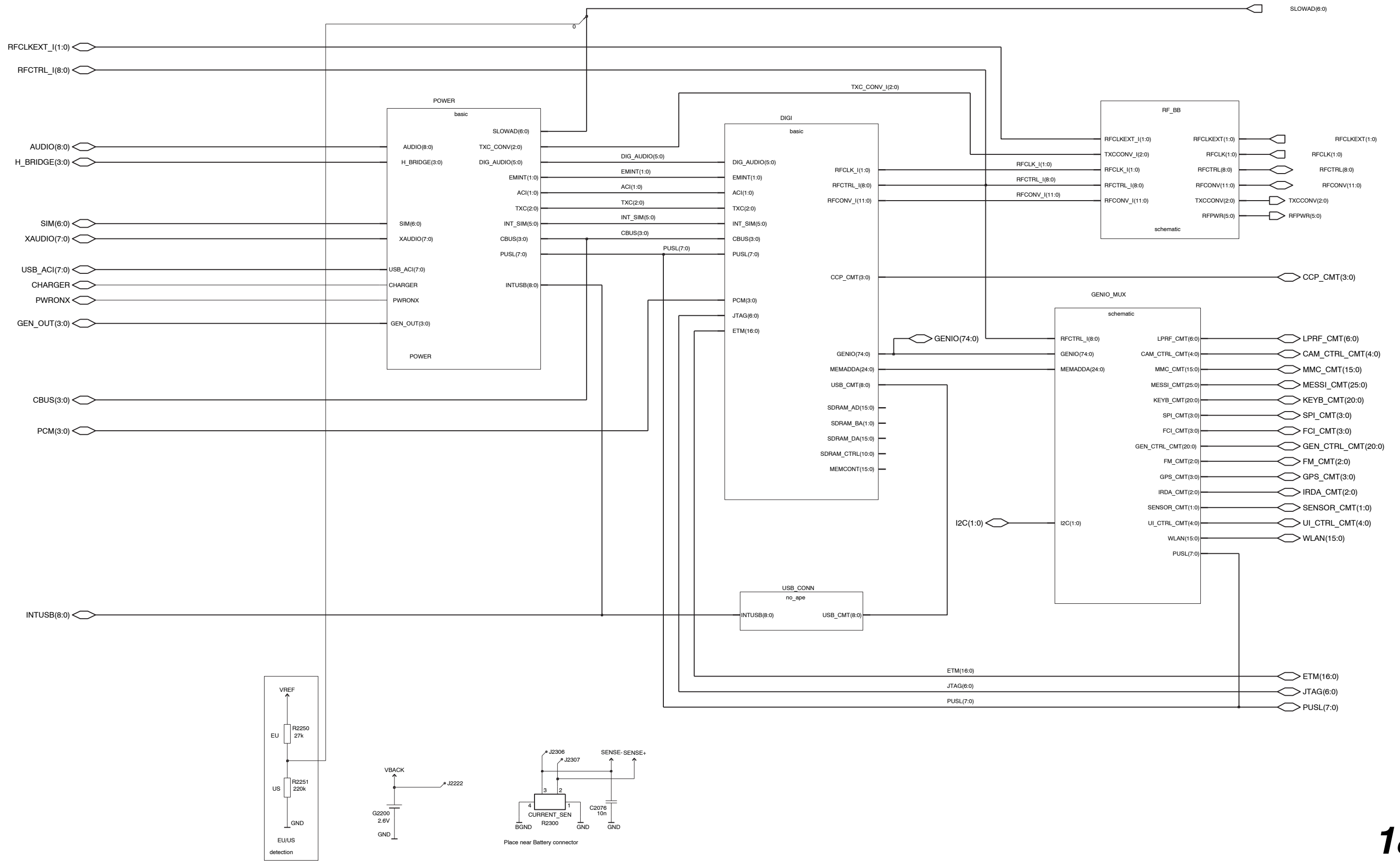


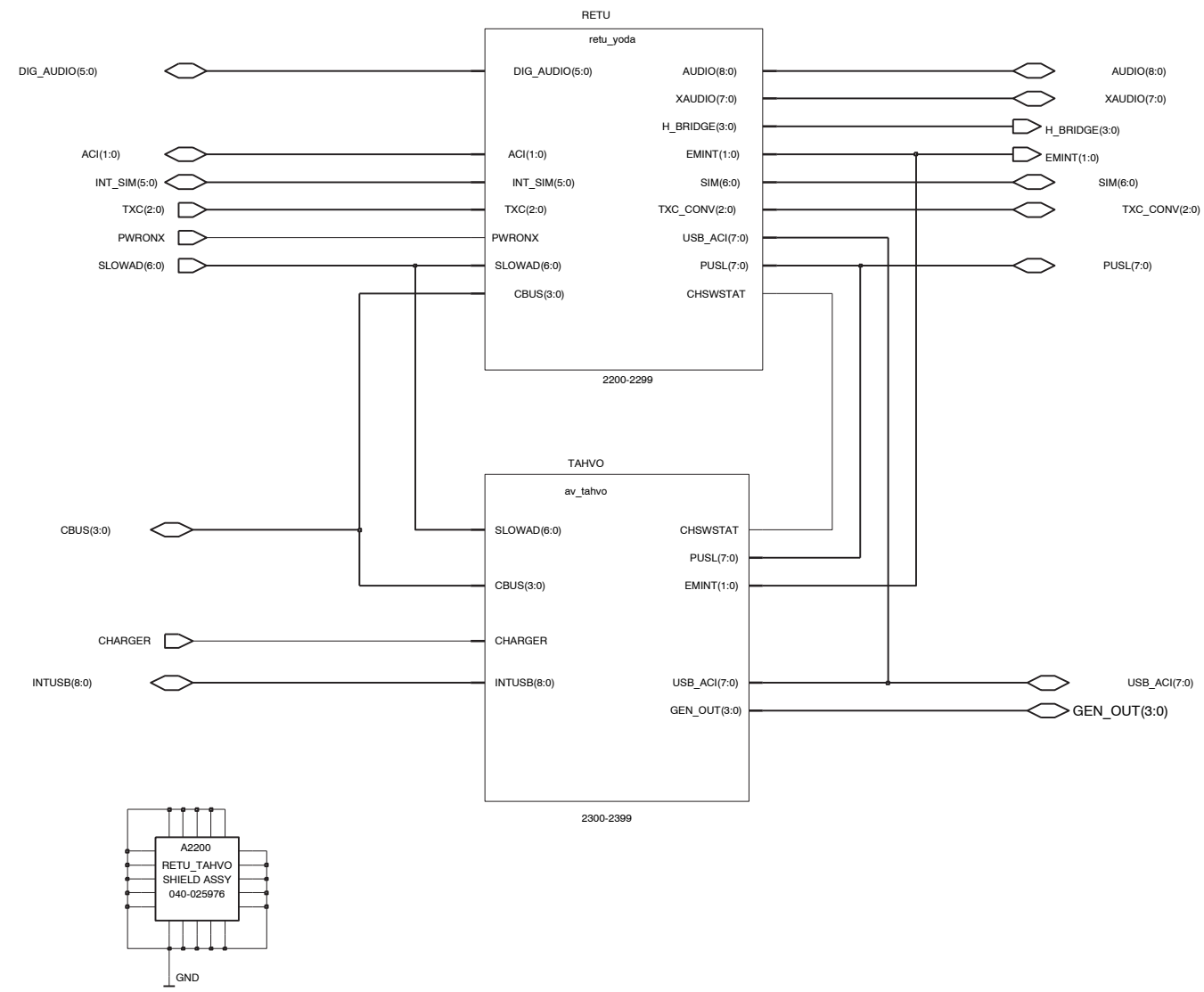


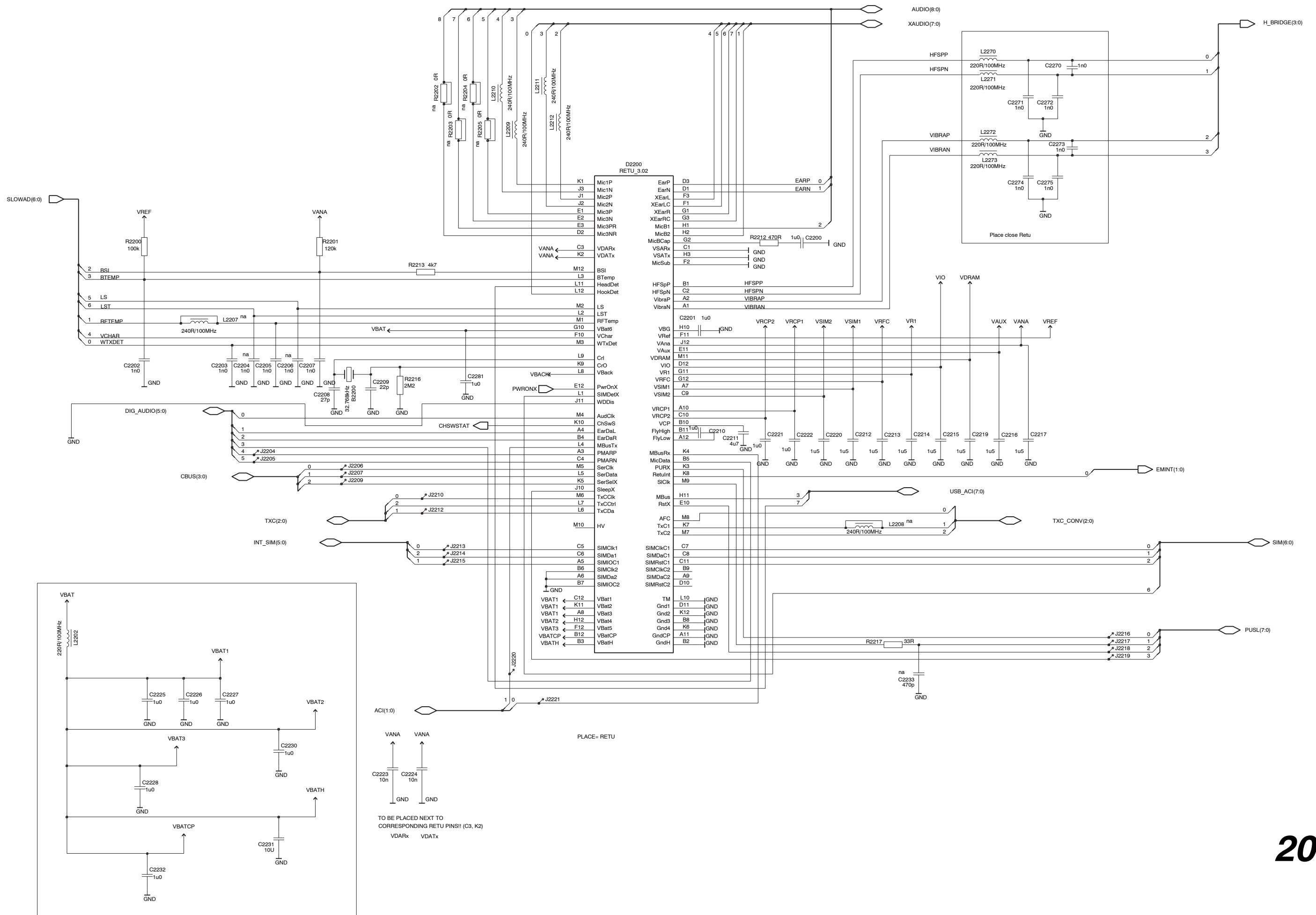


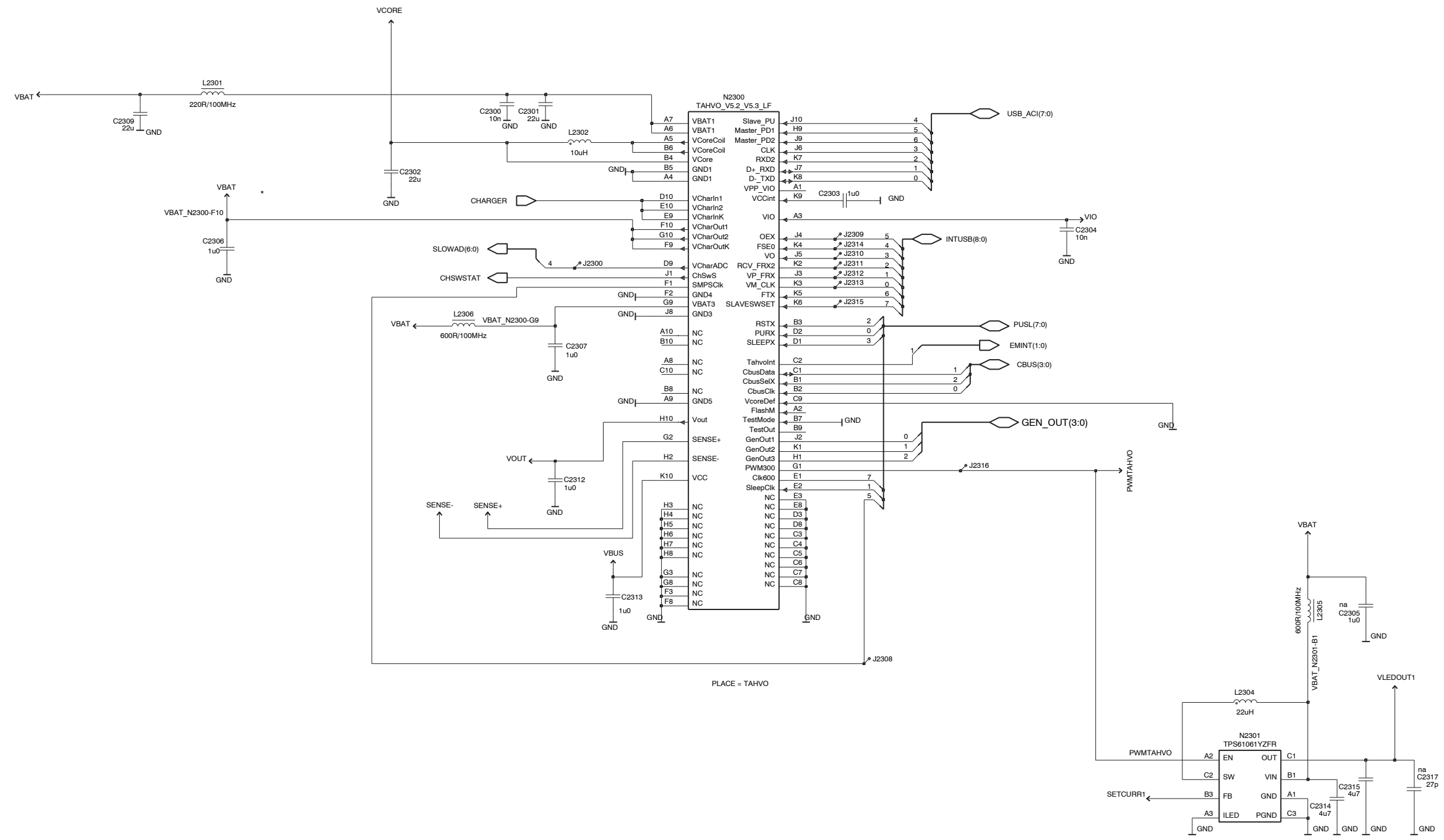


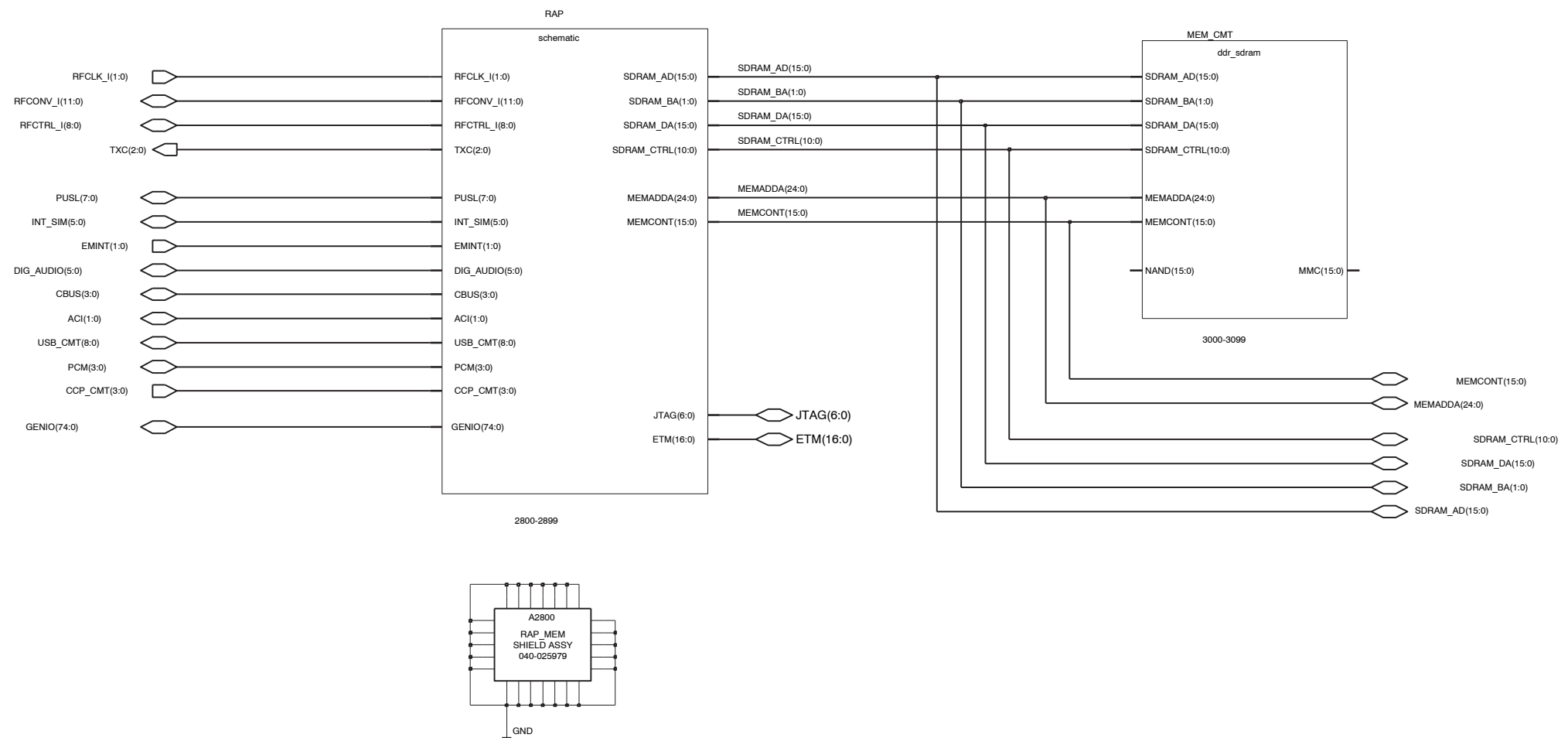


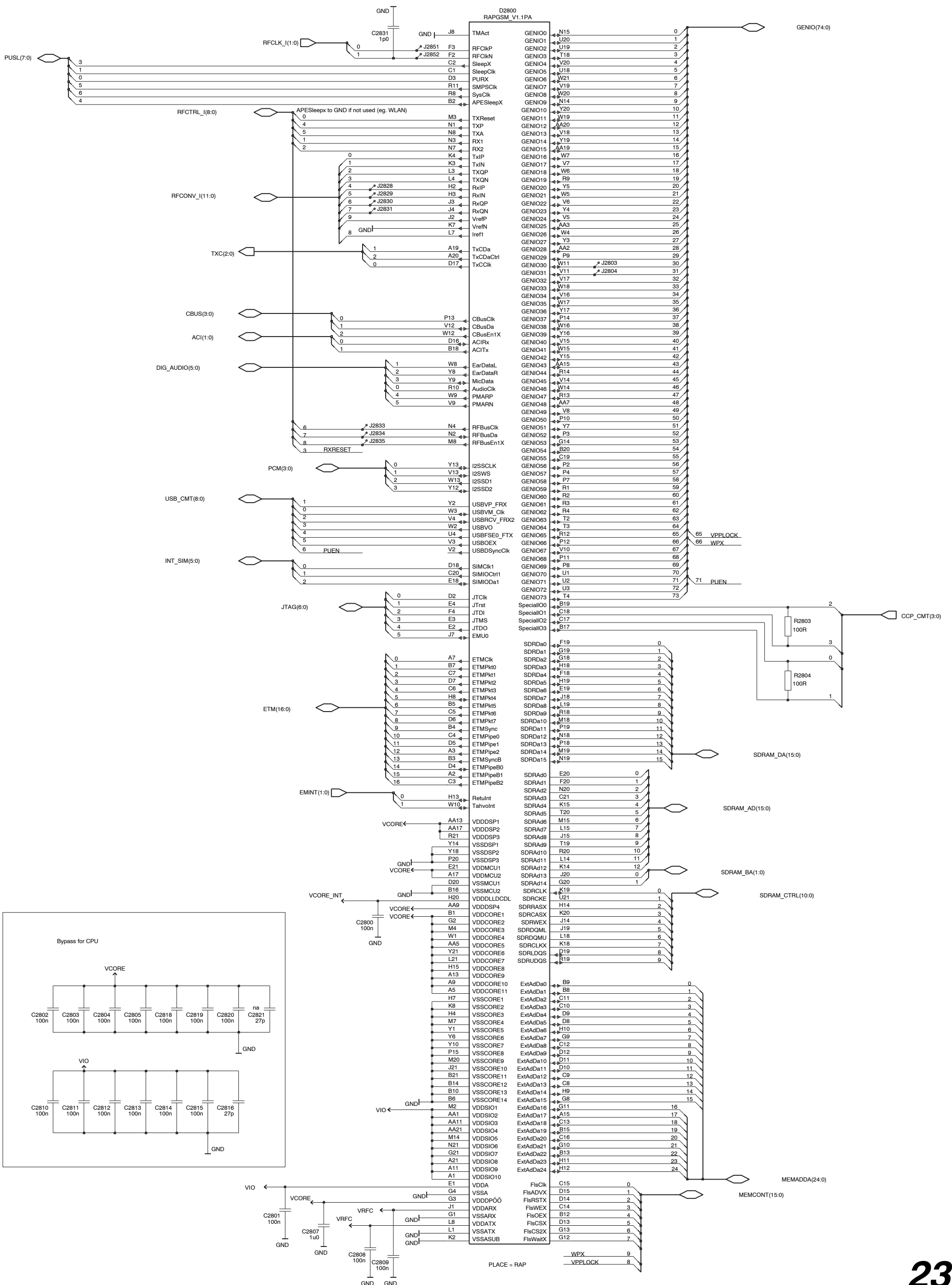


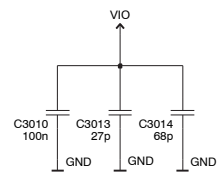
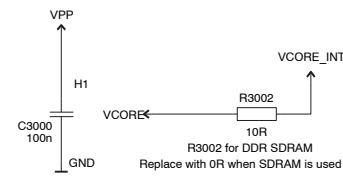
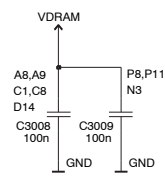
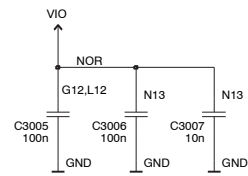
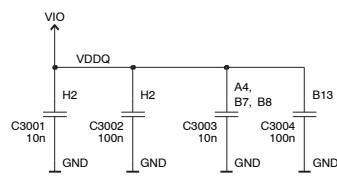
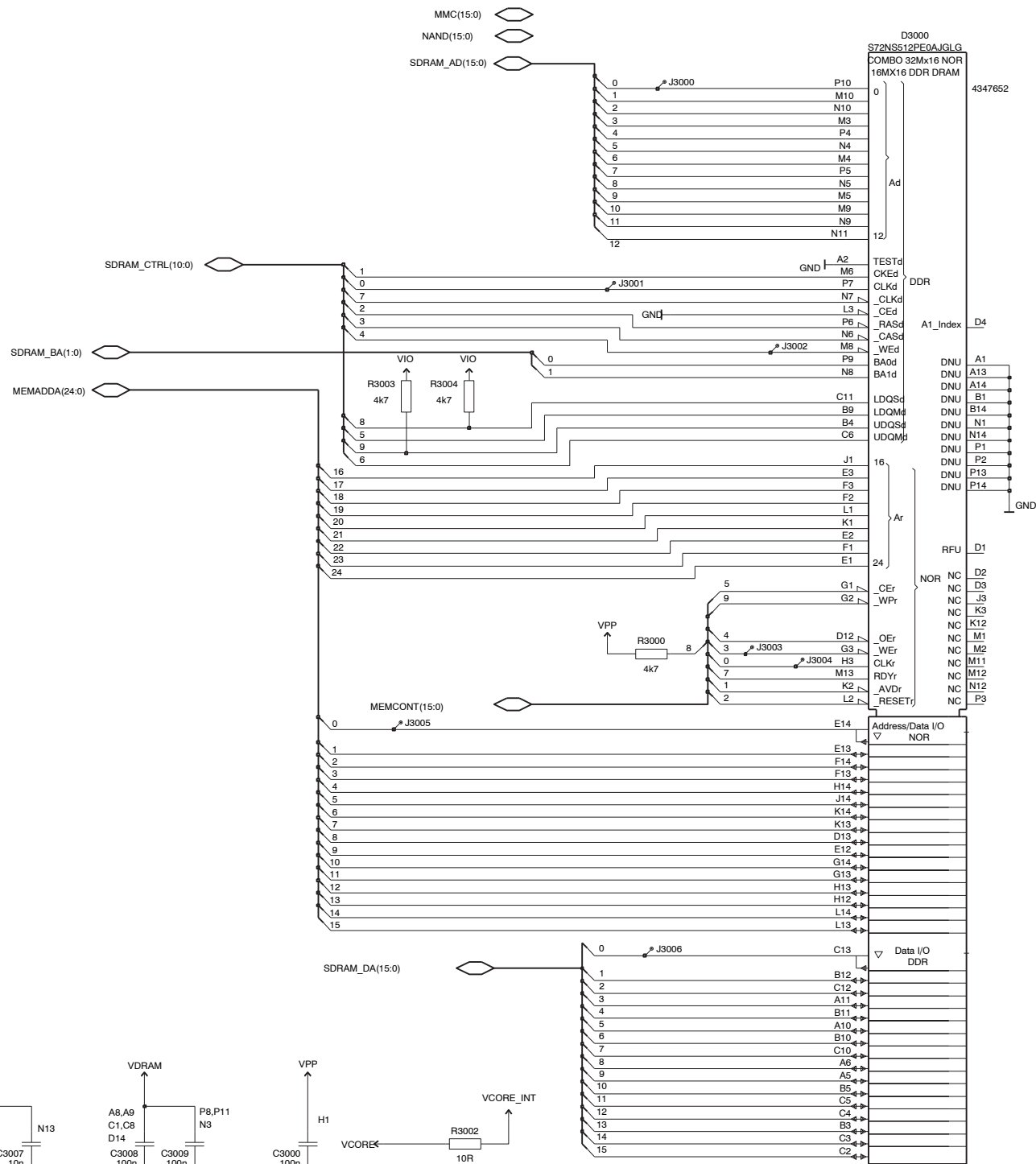








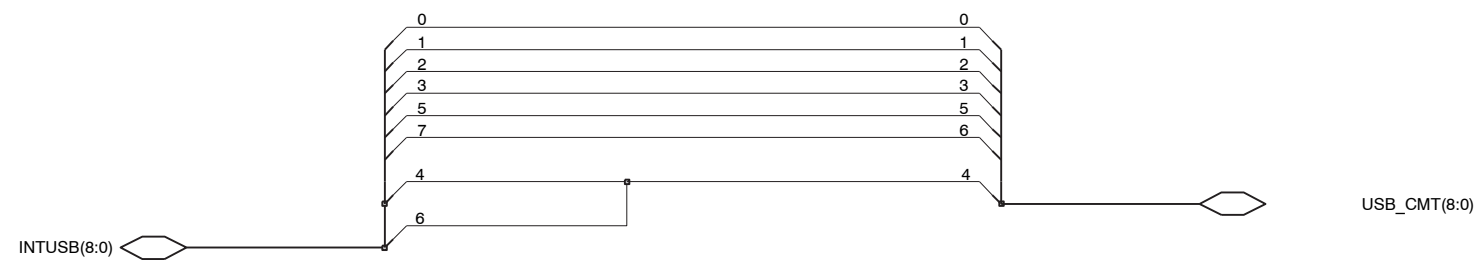




Place = D3000

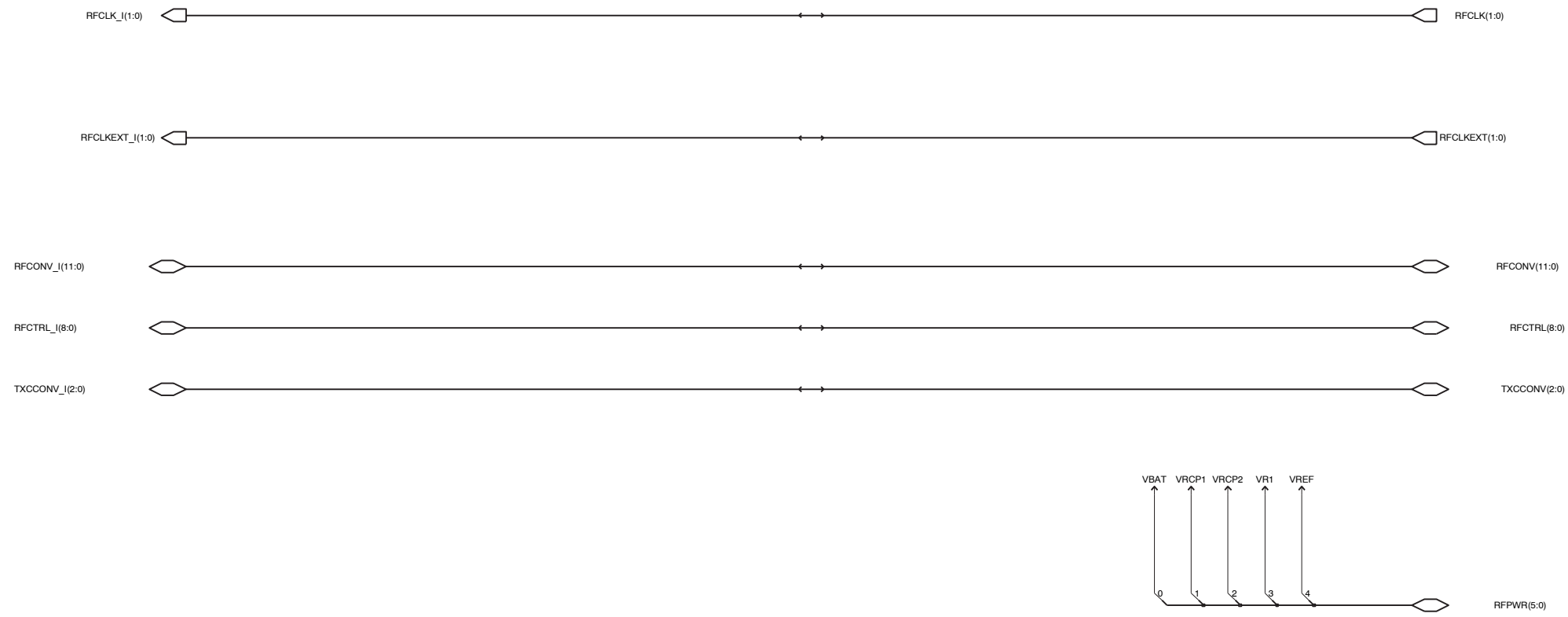
A8,A9,C1,C8,D14,N3,P8,P11= VDRAM/DRAM VDD  
 A4,B7,B8,B13= VIO DRAM VDDQ  
 H2,N13= VIO NOR VCC  
 G12,L12= VIO NOR VCCQ  
 H1= VPP NOR VPP  
 A7,B2,C7,C14,M7,N2,P12= GND DRAM VSS  
 A3,A12,B6,C9= GND DRAM VSSQ  
 J2,J12,M14,J13,F12= GND NOR VSS

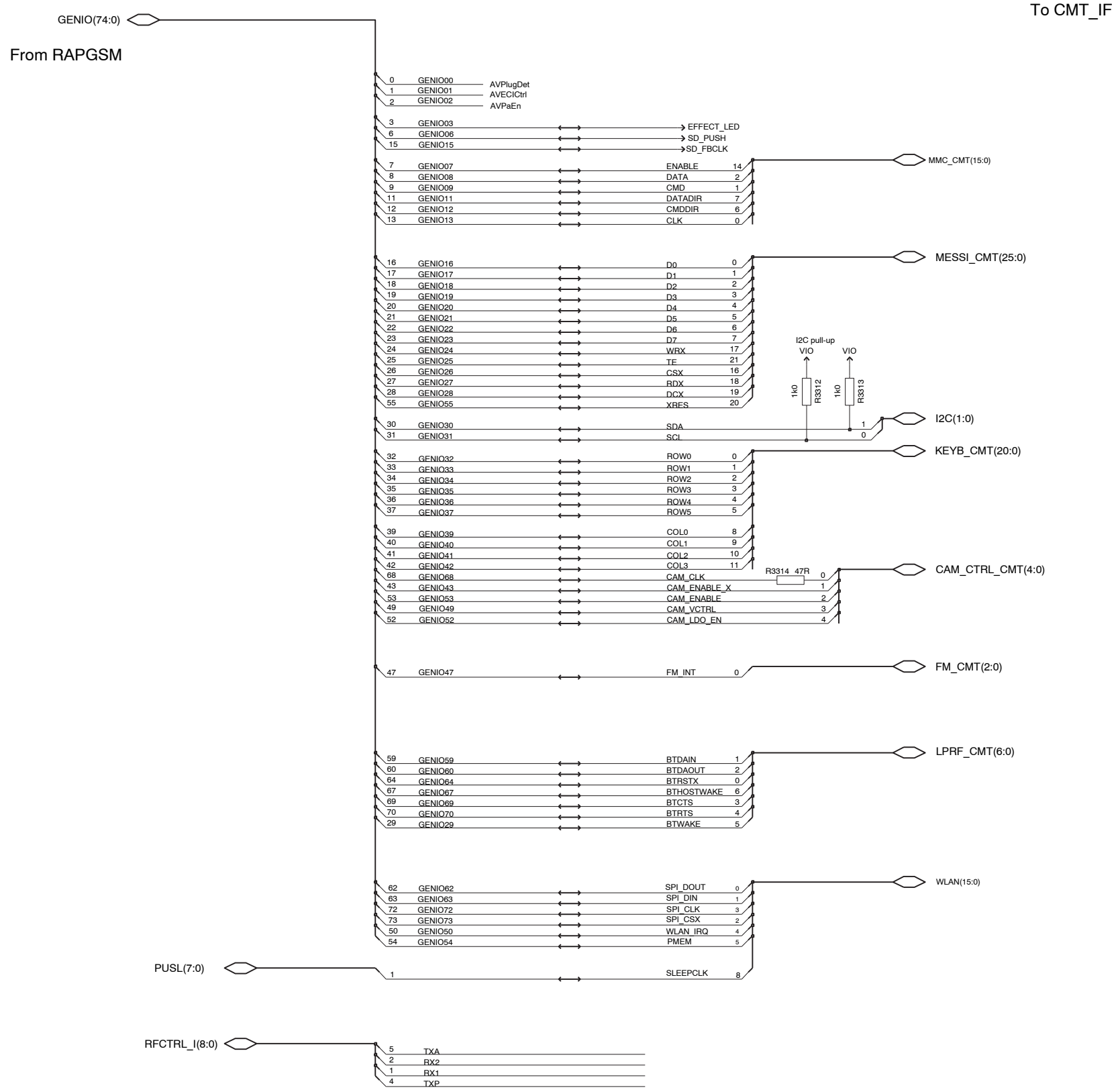




CBB5X

TOP SHEET





THESE ARE USED BY ENGINE

GENIO65 = VPPLOCK  
 GENIO66 = WPX  
 GENIO71 = PUEN

DO NOT CHANGE

- FCI\_CMT(3:0)
- GEN\_CTRL\_CMT(20:0)
- GPS\_CMT(3:0)
- MEMADDA(24:0)
- SPI\_CMT(3:0)
- UI\_CTRL\_CMT(4:0)
- SENSOR\_CMT(1:0)
- IRDA\_CMT(2:0)

